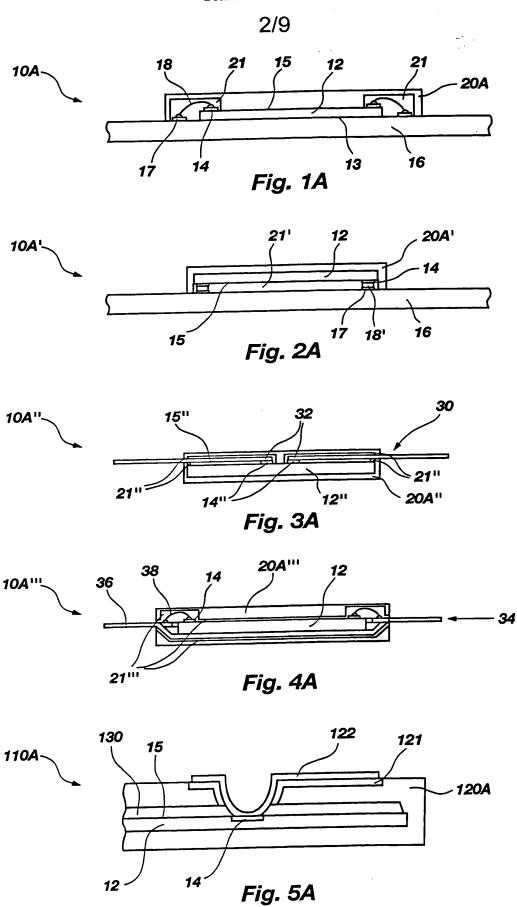
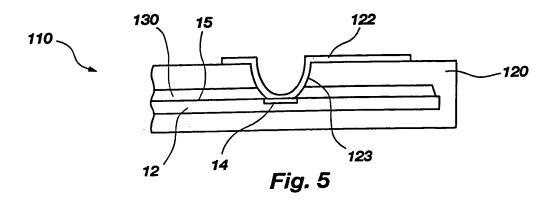


Fig. 4

Docket No.: 2269-4212.6US





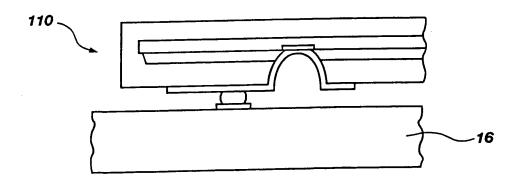


Fig. 6

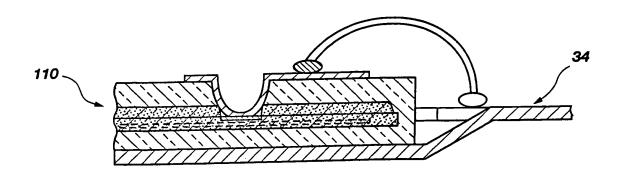


Fig. 7

TITLE: STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

Inventor: Warren M. Farnworth Docket No.: 2269-4212.6US

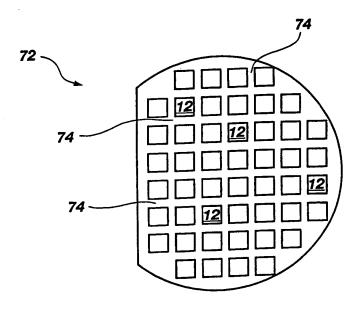


Fig. 8

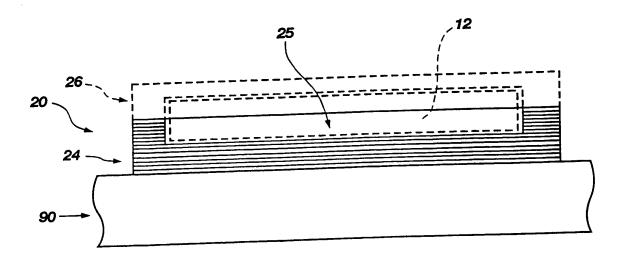
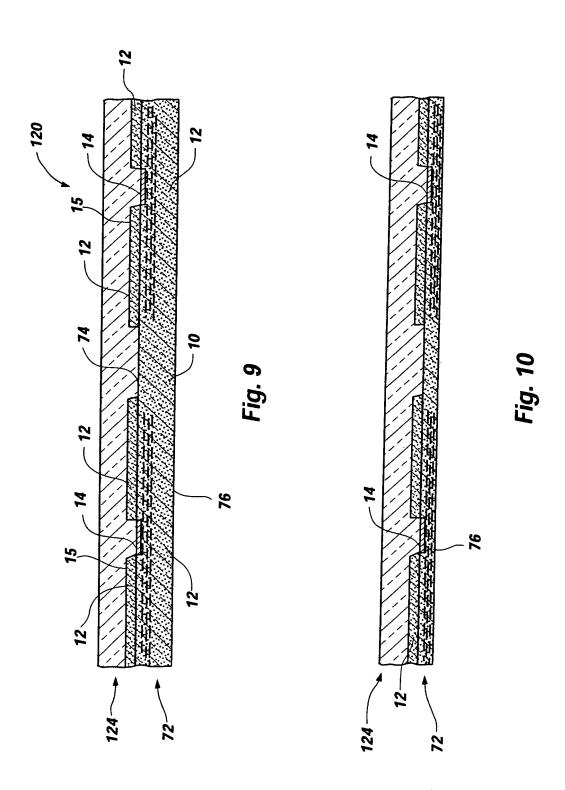
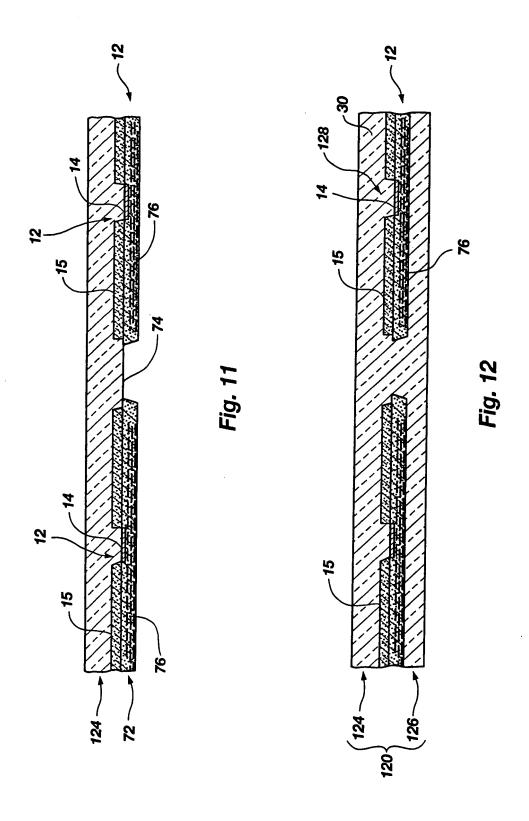
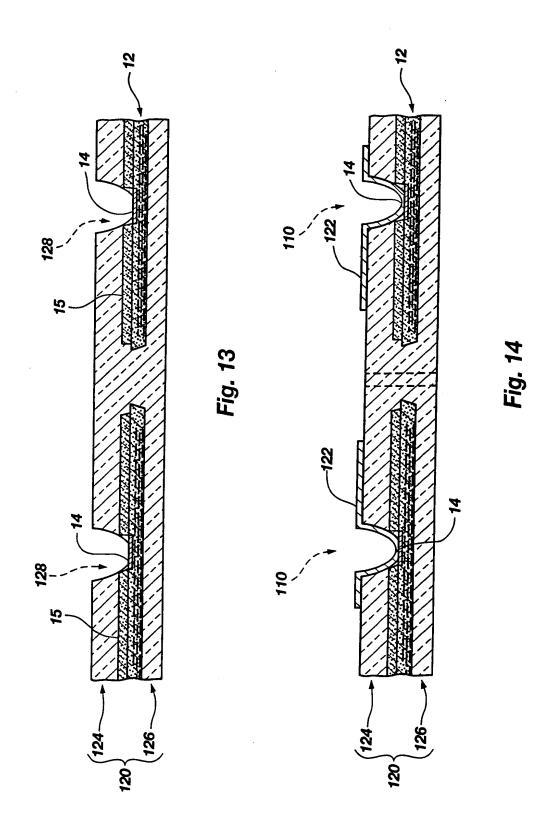


Fig. 18

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TITLE: STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES

Inventor: Warren M. Farnworth Docket No.: 2269-4212.6US

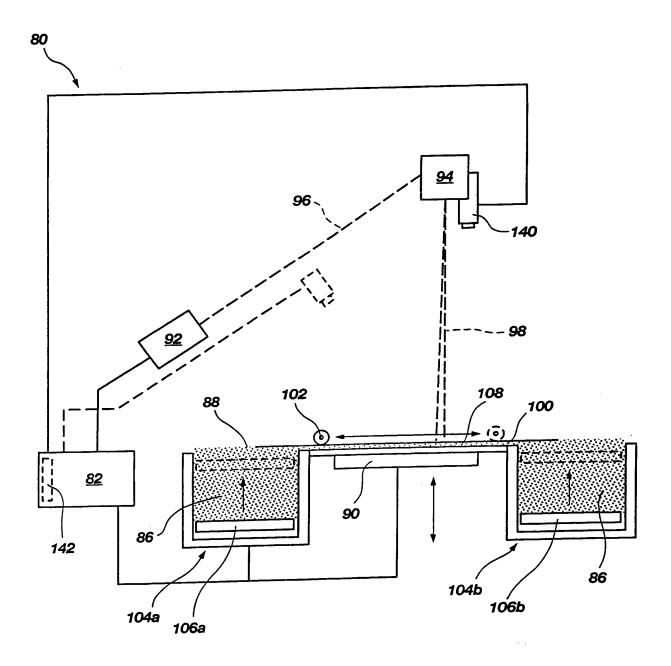


Fig. 15

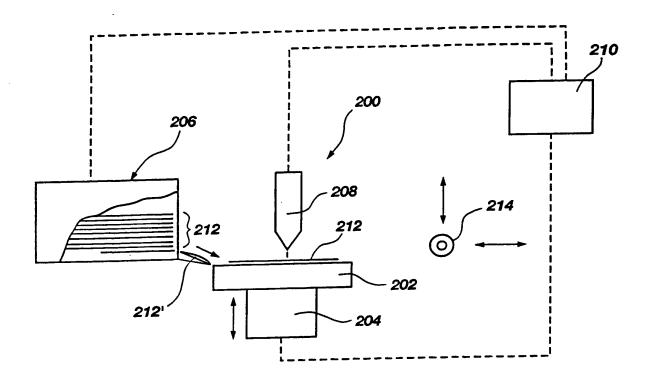


Fig. 16

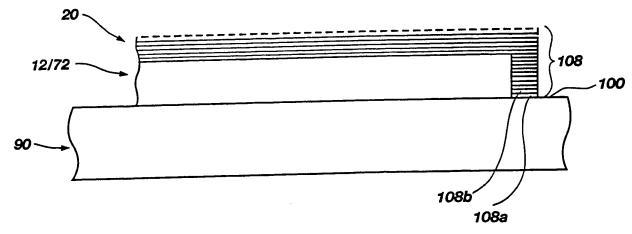


Fig. 17